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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re patent of

:

Kazuhiro NISHIKAWA et al.

:

Patent No. 6,971,167

10/611868

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Attorney Docket No. 2003\_0915A

Issued December 6, 2005

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MULTILAYERED CIRCUIT BOARD  
FORMING METHOD AND MULTILAYERED  
CIRCUIT BOARD

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**CITATION OF PRIOR ART**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.501, it is respectfully requested that the following publications be placed in the patent file of the above-identified patent.

Japanese Publication No. 2001-237542;

Japanese Publication No. 5-37128;

Japanese Publication No. 10-93233;


Japanese Publication No. 2000-236166; and

Japanese Publication No. 2001-332866.

The above references were cited in a corresponding counterpart Japanese application in an Office Action dated October 23, 2007.

Respectfully submitted,

Kazuhiro NISHIKAWA et al.

By 

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